



Diode Module

Features

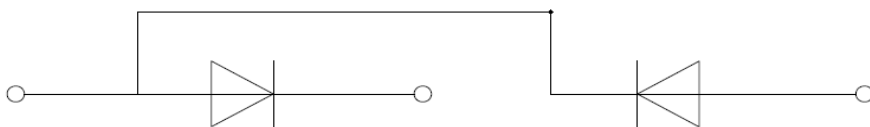
- A package of series of two diodes
- Heat transfer through alumina ceramic and metal substrate
- Welding by vacuum welding technology, which provide high reliability

Product Summary

Parameter	Value	Unit
V_{RRM}	2000	V
$I_{F(AV)}(@ T_C = 100^\circ\text{C})$	136	A
$I_{FSM}(@ t_p = 10\text{ms})$	3640	A
$V_F(\text{Max})$	1.60	V

Applications

- AC converter
- Inverter
- DC motor



Absolute Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Conditions	Symbol	Values	Unit
Repetitive peak reverse voltage	$T_{vj} = 25^\circ\text{C}$	V_{RRM}	2000	V
Non-repetitive peak reverse voltage	$T_{vj} = 25^\circ\text{C}$	V_{RSM}	2100	V
Average forward current	$T_C = 100^\circ\text{C}$	$I_{F(AV)}$	136	A
Forward surge current	1/2 cycle, Sine wave, 50Hz	I_{FSM}	3640	A
I^2t value for fusing	$T_{vj} = 25$	I^2t	66200	A^2s
RMS isolation voltage	A.C 50Hz(1s/1min)	V_{ISO}	3600/3000	V
Junction temperature range		T_J	-40 ~ +150	
Storage temperature range		T_{stg}	-40 ~ +125	

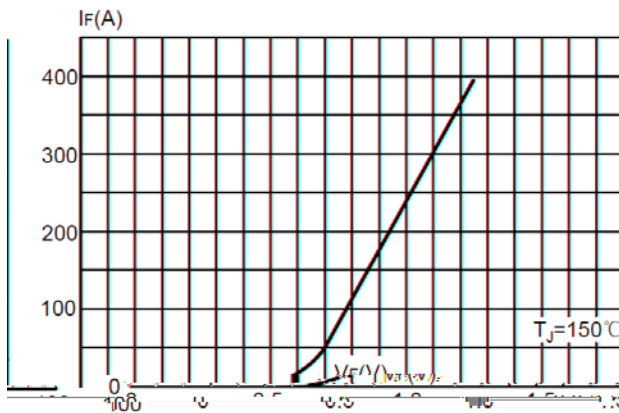
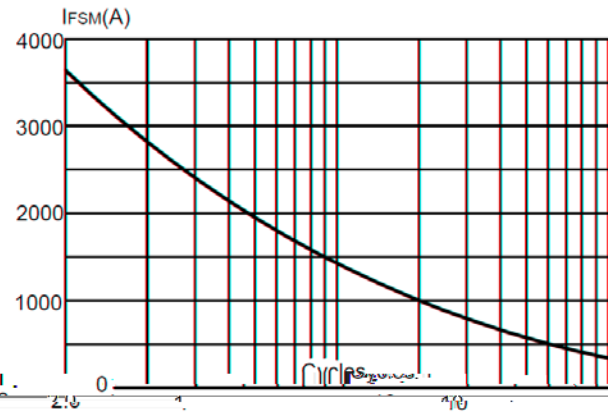
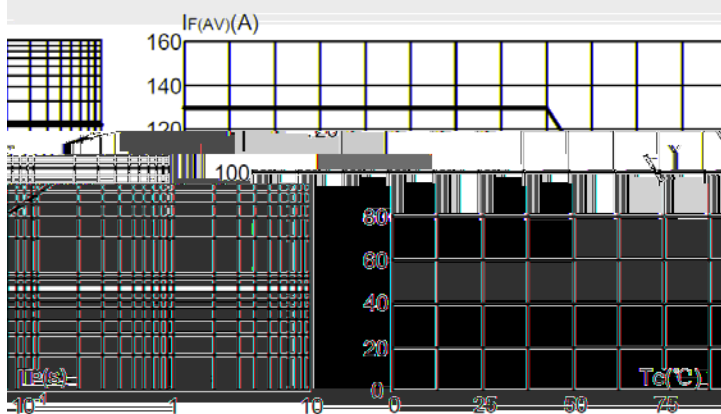
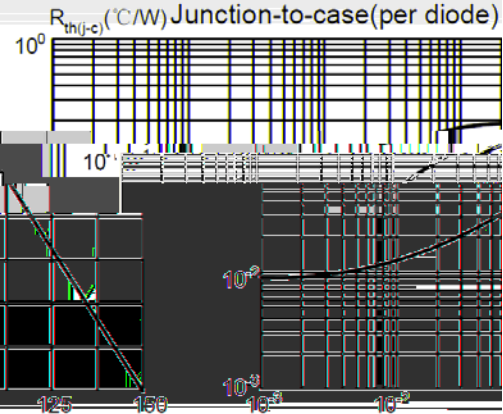
**Electrical Characteristics (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)**

Parameter	Conditions	Symbol	Values			Unit
			Min.	Typ.	Max.	
Peak forward voltage	$I_F=410\text{A}$, $t_P=380$	V_F			1.60	V
Reverse leakage current	$V_R = V_{RRM}$, $T_{vj} = 25$	I_{RRM}			100	A
	$V_R = V_{RRM}$, $T_{vj} = 150$				50	mA
Threshold voltage	$T_{vj} = 150$, for power loss calculation only	V_{TO}			0.85	V
Dynamic resistance		r_T			1.5	m

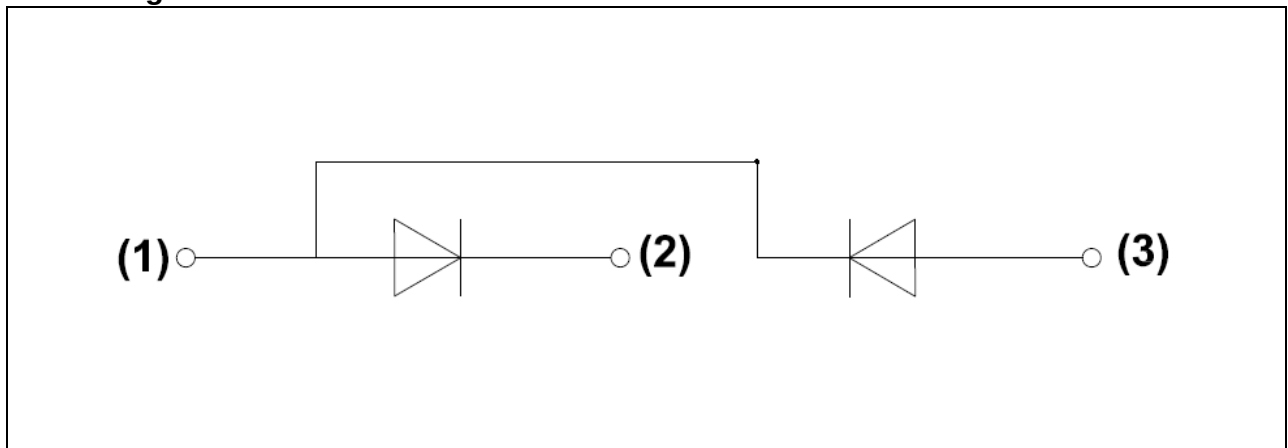
Thermal Characteristics (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Conditions	Symbol	Values			Unit
			Min.	Typ.	Max.	

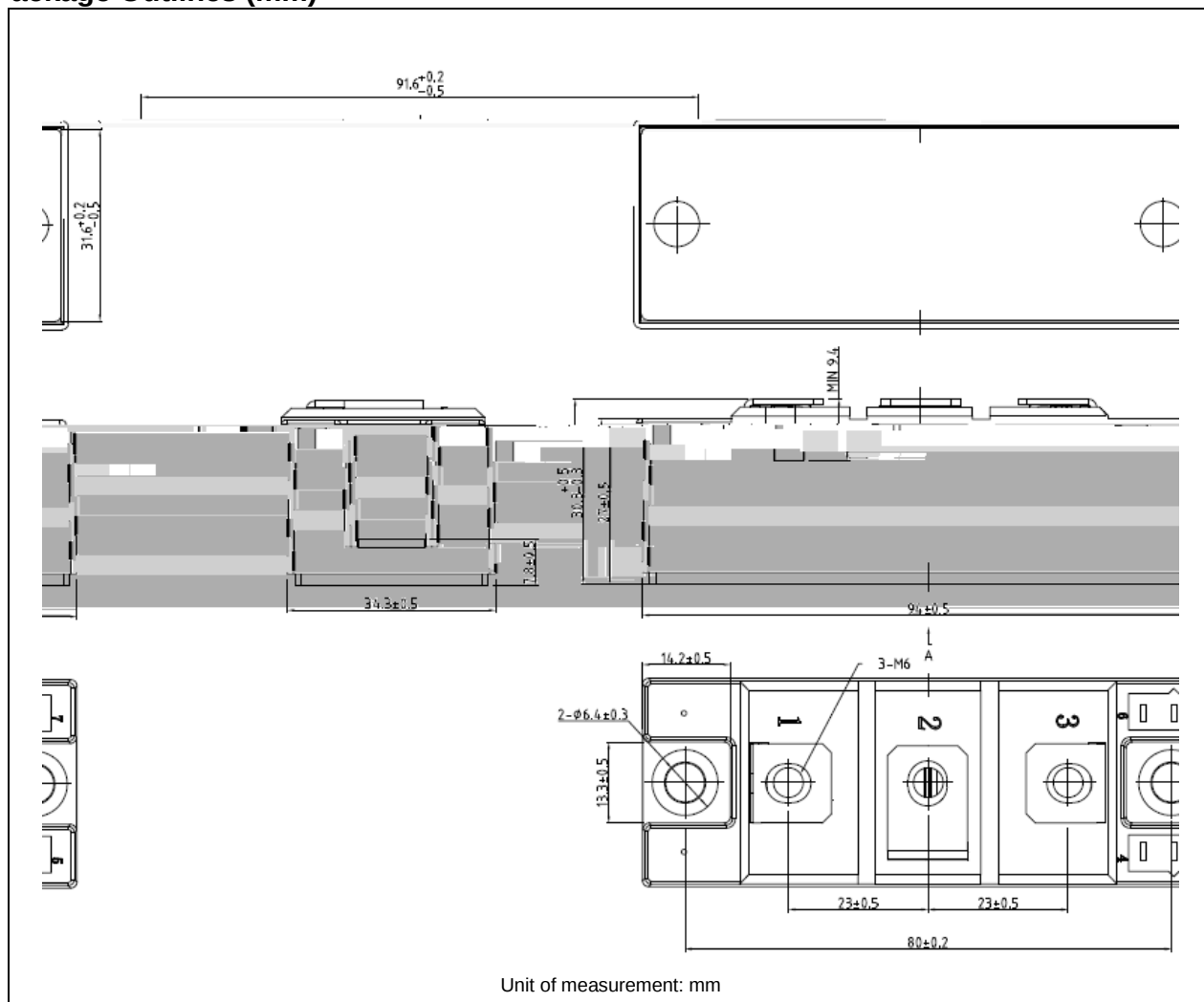
Typical Electrical & Thermal Characteristics

FIG 1: Forward characteristics(per diode)

FIG 2: Peak on-state surge current

FIG 3: Forward current vs case temperature

FIG 4: Junction-to-case thermal resistance


Circuit Diagram



Package Outlines (mm)





Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Semiconductor Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. This information in this document is subject to change without prior notice. Notwithstanding this, Jiangsu JieJie will fully comply with the terms outlined in a signed agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document is the initial release, dated 18 May 2026. This document supersedes and replaces all information previously supplied.

 is registered trademark of Jiangsu JieJie Semiconductor Co., Lt ©2026 Jiangsu JieJie Semiconductor Co., Ltd. All rights reserved.